

PROCESSOR CORE

24

26

23

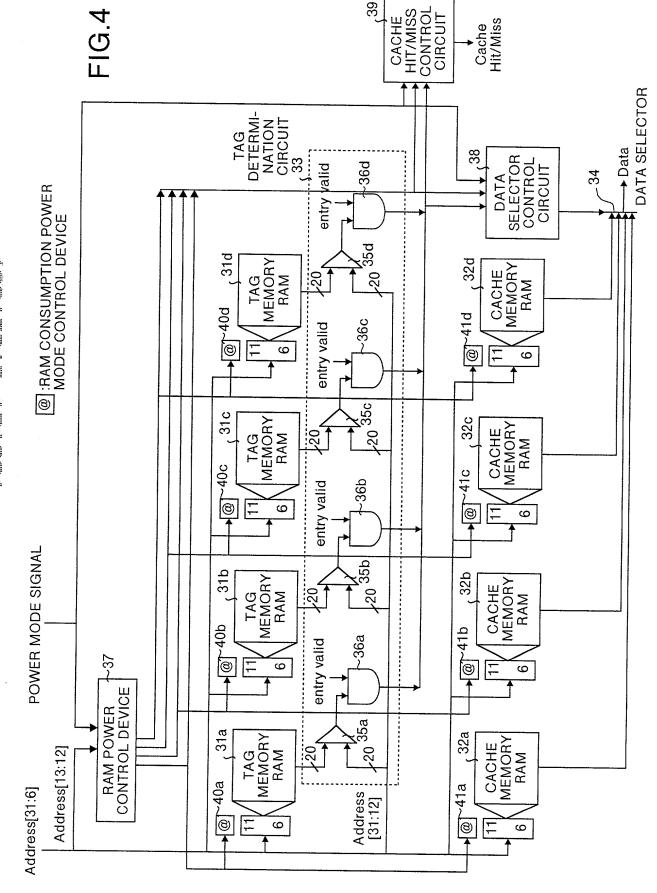
CACHE MEMORY SYSTEM

25

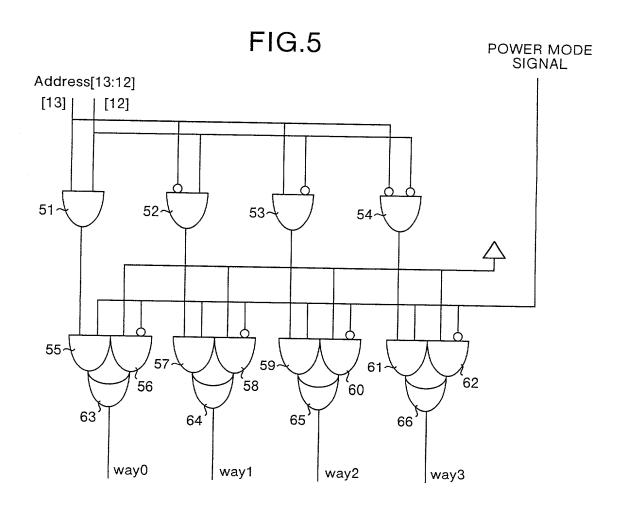
27

4

EXTERNAL MEMORY SYSTEM
(E.G., SDRAM)



The grant of the control of the cont



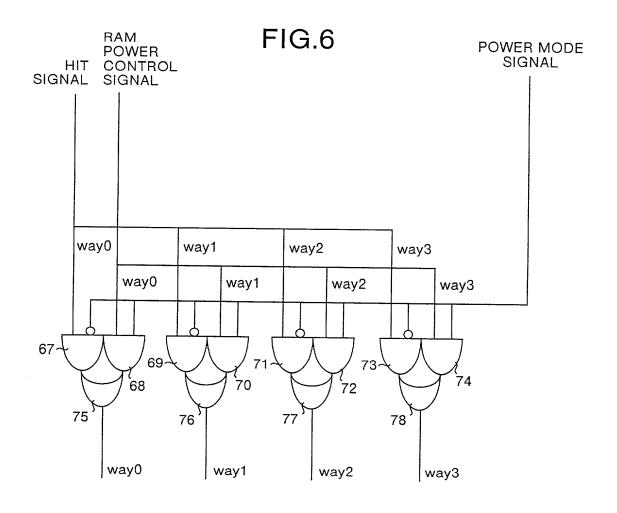
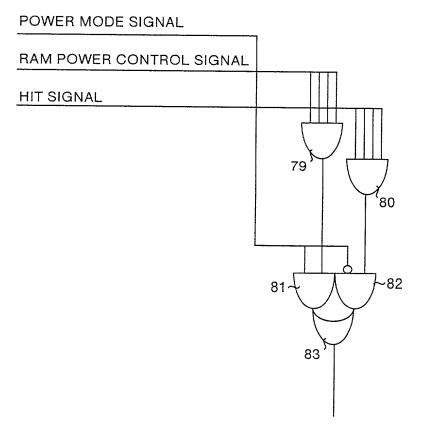
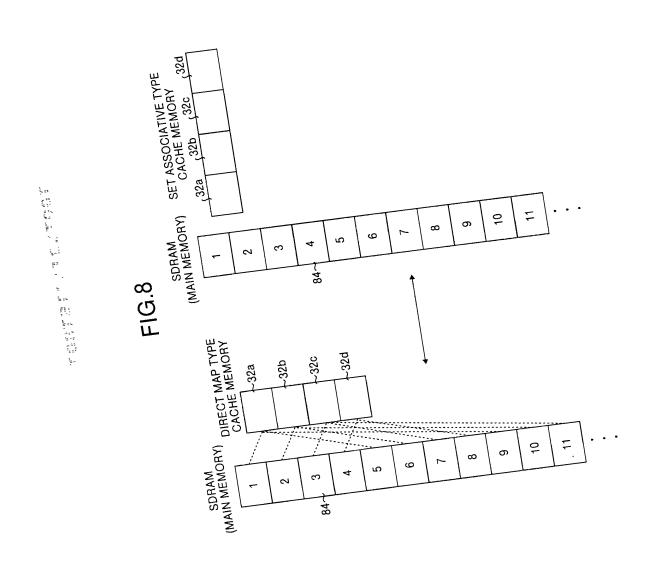
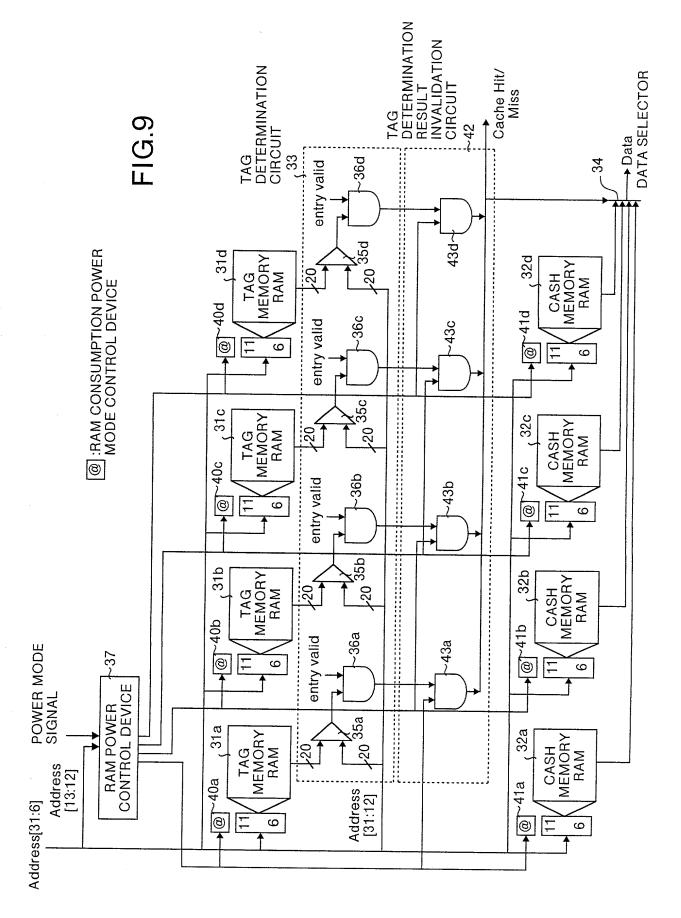


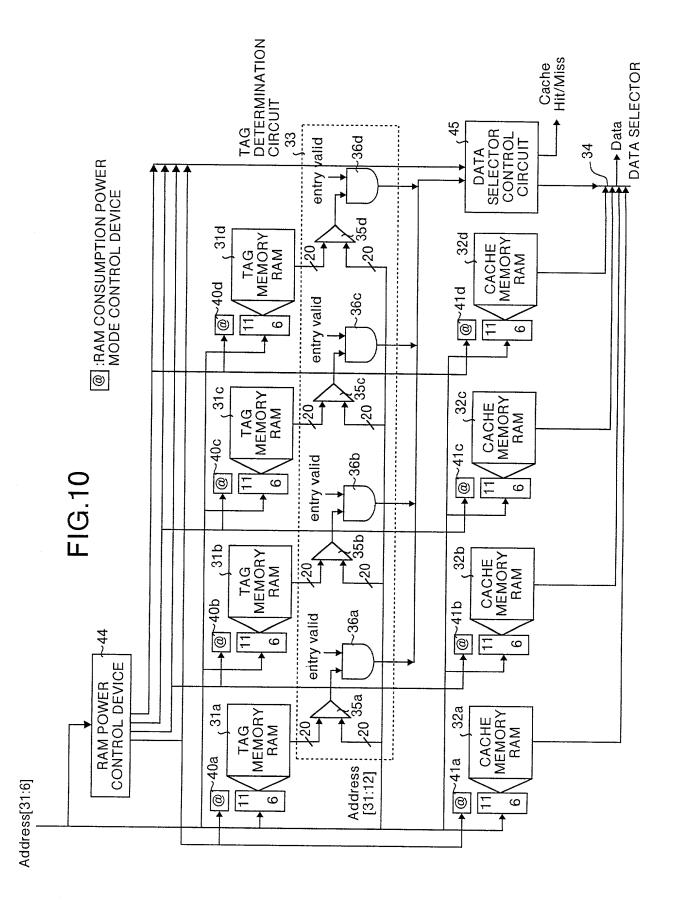
FIG.7

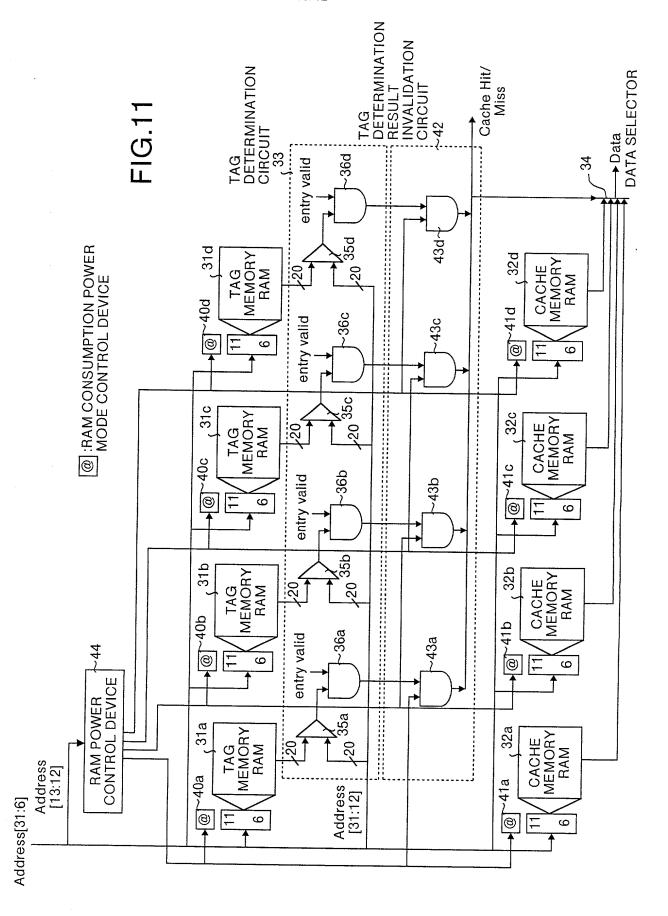


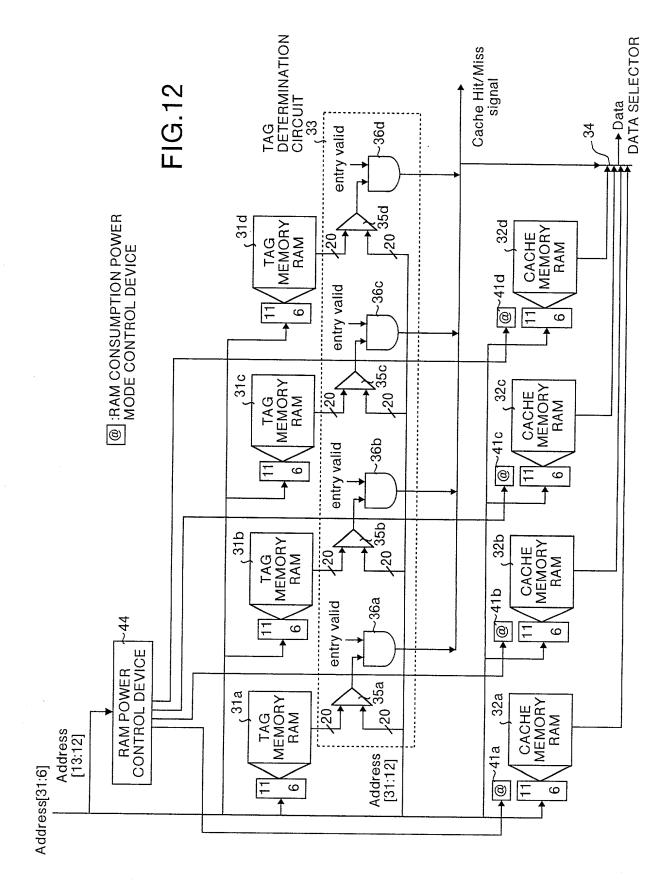


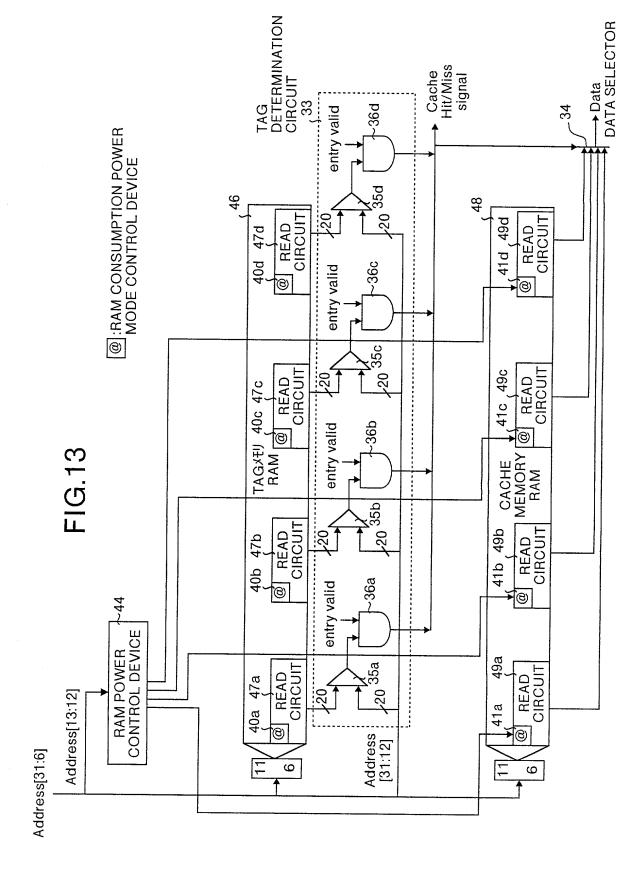












and the state of t